

**PERSONAL INFORMATION**

**Luca Aluigi**



**Profile** A proactive and enthusiastic individual with extensive knowledge in RF and mm-wave ICs in SiGe and GaAs technologies. Has creativity and vision to initiate new ideas and implement innovative approaches. Versatile and adaptable, has calm and methodical approach to problem solving.

**Sex** Male  
**Date of birth** 07/08/1978  
**Nationality** Italian

**WORK EXPERIENCE**

June 2019 – present	<b>Senior RFIC/MMIC Designer</b>
	Huawei Technologies, Milan R&D Center, Segrate, Italy ▪ Millimeter-wave IC design in GaAs and SiGe technologies for wireless backhaul <b>Business or sector</b> Integrated circuits design
Feb. 2018 – June 2019	<b>Research Fellow</b>
	University of Modena and Reggio Emilia, DISMI, Reggio Emilia, Italy ▪ Millimeter-wave IC design in SiGe technology within the EC funded project TARANTO <b>Business or sector</b> Integrated circuits design
Nov. 2016 – Feb. 2018	<b>RF HW Designer</b>
	ASK Industries Spa, R&D Department, Reggio Emilia, Italy ▪ Microwave antenna and RF front-end systems design <b>Business or sector</b> Automotive RF electronic systems design
Sep. 2014 – Sep. 2016	<b>Marie-Curie Research Fellow</b>
	Tyndall National Institute, Theory Modelling & Design Centre, Cork, Ireland ▪ Millimeter-wave IC design in SiGe technology within the EC funded project FLARES <b>Business or sector</b> Integrated circuits design
Sep. 2013 - Aug. 2014	<b>Program Management Trainee</b>
	Cadence Design Systems, Feldkirchen, Munich, Germany ▪ Run and leverage relationships and collaborations between academia and industry <b>Business or sector</b> Semiconductor CAD
Feb. 2013 - July 2013	<b>Design Services Engineer</b>
	RFMD (UK) Ltd, Newton Aycliffe, Co. Durham, United Kingdom ▪ Development and maintenance of GaAs MMIC Design-Kits <b>Business or sector</b> Semiconductor manufacture
Jan. 2012 - Jan. 2013	<b>Design Engineer</b>
	University of Perugia, Department of Information and Electronic Engineering, Perugia, Italy ▪ Post-doc activity of IC design in SiGe technology within the EC funded project ARTEMOS <b>Business or sector</b> Integrated circuits design

**EDUCATION AND TRAINING**

Jun. 2011 - Dec. 2011	<p><b>Fulbright Scholar</b>                  Georgia Tech Institute, GEDEC (Georgia Electronic Design Centre), Atlanta, GA, USA</p> <ul style="list-style-type: none"> <li>Activity: Heterogeneous integration of MMIC and antennas on LCP substrate through a contactless approach, for imaging applications</li> <li>Supervisor: Prof. Manos M. Tentzeris</li> </ul>
Nov. 2008 - Oct. 2011	<p><b>PhD Degree in Information Engineering (viva date: 2 Feb. 2012)</b>                  University of Perugia, Department of Information and Electronic Engineering, Perugia, Italy</p> <ul style="list-style-type: none"> <li>Thesis title: "Development of mm-wave SiGe BiCMOS SoC space-based radiometers"</li> <li>Advisors: Prof. Federico Alimenti, Prof. Luca Roselli</li> </ul>
Nov. 1997 - Jul. 2008	<p><b>Master's Degree in Electronics Engineering (viva date: 18 Jul. 2008)</b>                  University of Perugia, Department of Information and Electronic Engineering, Perugia, Italy</p> <ul style="list-style-type: none"> <li>Thesis title: "24 GHz radar for traffic control. Design of the I/Q mixer in 0.25 um SiGe BiCMOS"</li> <li>Advisors: Prof. Federico Alimenti, Prof. Luca Roselli</li> </ul>
Sep. 1993 - Jul. 1997	<p><b>High School Diploma</b>                  Liceo Classico "Plinio il Giovane", Citta' di Castello, Italy</p>

**PERSONAL SKILLS**

Mother tongue	Italian				
	UNDERSTANDING		SPEAKING		WRITING
Other language(s)	Listening	Reading	Spoken interaction	Spoken production	
English*	C1	C1	C1	C1	C1
German	A1	A1	A1	A1	A1
*FCE Certificate obtained from British Academy in March 2007.					
Levels: A1/2: Basic user - B1/2: Independent user - C1/2 Proficient user Common European Framework of Reference for Languages					

Communication skills	Very good communication and team working skills developed through experiences in technical and non-technical areas, including working as volunteer in the cultural and social-services fields.
Organisational / managerial skills	Excellent organisational and managerial skills, developed through the work experiences listed above and also in side activities such as the management of music festivals in central Italy.
Job-related skills	Use of measurements lab. tools (VNAs, spectrum analysers, signal generators, solder stations, etc.);
Computer skills	Excellent computer skills on both Win and Mac and Linux systems: <ul style="list-style-type: none"> <li>Electronic and electromagnetic CAD: Cadence Virtuoso, Calibre, PVS, QRC, Keysight ADS, EMpro, Ansys HFSS, AWR Microwave Office, EmSight, CST Microwave Studio</li> <li>Matlab, Mathematica, Origin</li> </ul>
Other skills / Interests	I enjoy percussions/drums playing, listening to music, playing tennis, reading, biking and travelling.

**ADDITIONAL INFORMATION**

Driving licence	European/Italian A, B
Professional memberships	IEEE Senior Member n. 90539596
Publications	<a href="http://scholar.google.it/citations?user=0JjOsJYAAAAJ&amp;hl=en">http://scholar.google.it/citations?user=0JjOsJYAAAAJ&amp;hl=en</a>